



Docket No.: M4065.0960/P960  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Alexander Krymski

Allowed: October 10, 2006

Application No.: 10/705,843

Confirmation No.: 4875

Filed: November 13, 2003

Art Unit: 2878

For: PIXEL SIGNAL BINNING AND  
INTERPOLATION IN COLUMN  
CIRCUITS OF A SENSOR CIRCUIT

Examiner: T. X. Luu

**COMMENTS ON STATEMENT OF REASONS  
FOR ALLOWANCE UNDER 37 CFR §1.104(E)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant has reviewed the Examiner's Statement of Reasons for Allowance with the October 10, 2006 Notice of Allowance and Allowability regarding the above-identified application. Entry of the Statement into the record should not be construed as any agreement with or acquiescence in the reasoning stated by the Examiner. Each of the claims stands on its own merits and is patentable because of the combination it recites and not because of the presence or absence of any one particular element.

The Examiner's Statement was not prepared by Applicant and only contains the Examiner's possible positions in one or more reasons for allowability. Thus, any

interpretation with respect to the Examiner's Statement of Reasons for Allowance should not be imputed to the Applicant.

Dated: November 2, 2006

Respectfully submitted,

By  \_\_\_\_\_

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